

~~ABSTRACT~~

An increase in overall resistance value is prevented by having different contact resistance connecting between different wiring layers. A transistor has a
5 first conductive layer having a first resistivity formed on an impurity diffusion regions, a first contact hole group connecting the first conductive layer and the impurity diffusion region, a second conductive layer having a second resistivity formed on the first conductive layer, and a second contact hole group connecting the first conductive layer and the second conductive layer at an
10 upper part of the impurity diffusion region. The first contact hole group and the second contact hole group have a different total number of contact holes.